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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Friday 12 February 2021

Parallel session - ICTP Project: Digital Pulse Processor based on SoC-FPGA for Particle Detectors - Online (14:00-17:00)

Chair: *Maria Liz CRESPO (ICTP)*

| time | title | presenter |
|-------|---|--|
| 14:00 | ComBlock for Reconfigurable Virtual Instrumentation (RVI) | WERNER FLORIAN |
| 15:00 | Project activities with tutors | LUIS GARCIA ORDONEZ WERNER FLORIAN BRUNO VALINOTI ROMINA MOLINA RODRIGO MELO |