Table of contents

i

Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Monday 15 February 2021

Parallel session - ICTP Project: Digital Pulse Processor based on SoC-FPGA for Particle Detectors - Online (11:00-17:00)

Chair: Andres Cicuttin (ICTP)

time	title	presenter
11:00	Data acquisition system based on SoC-FPGA	LUIS GARCIA ORDONEZ
12:00	Project activities with tutors	
13:00	Break	
14:00	Project activities with tutors	LUIS GARCIA ORDONEZ WERNER FLORIAN BRUNO VALINOTI ROMINA MOLINA