

Joint ICTP-IAEA School on FPGA-based SoC and its Applications for
Nuclear and Related Instrumentation | (smr 3562)

Contribution ID : 650

Type : **not specified**

DC stabilization and Base-Line Restoration

Monday, 15 February 2021 11:00 (1:00)

Content

Summary

Presenter(s) : MLADEN BOGOVAC (IAEA, Austria); VLADIMIR RAJOVIC (University of Belgrade, Serbia)

Session Classification : Parallel session - IAEA Project: Data Acquisition System for Radiotracer Techniques in Industrial Applications and Environmental Monitoring