



# Table of contents

Tuesday 16 February 2021 .....	1
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# Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

## Tuesday 16 February 2021

### Parallel session - ICTP Project: Digital Pulse Processor based on SoC-FPGA for Particle Detectors - Online (11:00-17:00)

**Chair: Romina MOLINA (National University of San Luis, Argentina)**

time	title	presenter
11:00	Digital Pulse Processor (DPP) based on SoC-FPGA	BRUNO VALINOTI
12:00	Project activities with tutors	
13:00	Break	
14:00	Project activities with tutors	LUIS GARCIA ORDONEZ WERNER FLORIAN BRUNO VALINOTI ROMINA MOLINA