

**Joint ICTP-IAEA School on FPGA-based SoC and its Applications for
Nuclear and Related Instrumentation | (smr 3562)**

Contribution ID : **660**

Type : **not specified**

Project activities with tutors

Wednesday, 17 February 2021 12:00 (1:00)

Content

Summary

Session Classification : Parallel session - ICTP Project: Digital Pulse Processor based on SoC-FPGA for Particle Detectors