## Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Contribution ID: 662 Type: not specified

## Pile-up Correction and Dead Time Measurement

Wednesday, 17 February 2021 11:00 (1:00)

Content

Summary

**Presenter(s)**: MLADEN BOGOVAC (IAEA, Austria); VLADIMIR RAJOVIC (University of Belgrade, Serbia)

**Session Classification**: Parallel session - IAEA Project: Data Acquisition System for Radiotracer Techniques in Industrial Applications and Environmental Monitoring