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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Wednesday 17 February 2021

Parallel session - IAEA Project: Data Acquisition System for Radiotracer Techniques in Industrial Applications and Environmental Monitoring - Online (11:00-17:00)

Chairs: Francois FOULON (IAEA, Austria), Mladen BOGOVAC (IAEA, Austria)

time	title	presenter
11:00	Pile-up Correction and Dead Time Measurement	MLADEN BOGOVAC VLADIMIR RAJOVIC
12:00	Project activities with tutors	MLADEN BOGOVAC VLADIMIR RAJOVIC NIKOLA JOVALEKIC
13:00	Break	
14:00	Open-source Toolchain for ARM-based Microcontrollers: Methodology and Implementation	NIKOLA JOVALEKIC
16:00	Project activities with tutors	NIKOLA JOVALEKIC MLADEN BOGOVAC