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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Thursday 18 February 2021

Parallel session - ICTP Project: Digital Pulse Processor based on SoC-FPGA for Particle Detectors - Online (11:00-17:00)

Chair: Maria Liz Crespo (ICTP)

time	title	presenter
11:00	Project integration: Data acquisition and digital pulse processing with RVI communication block for particle detectors	LUIS GARCIA ORDONEZ BRUNO VALINOTI WERNER FLORIAN
12:00	Project activities with tutors	LUIS GARCIA ORDONEZ WERNER FLORIAN BRUNO VALINOTI ROMINA MOLINA
13:00	Break	
14:00	DEMO with a PMT detector	LUIS GARCIA ORDONEZ BRUNO VALINOTI WERNER FLORIAN
15:00	Project activities with tutors	LUIS GARCIA ORDONEZ WERNER FLORIAN BRUNO VALINOTI ROMINA MOLINA