Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

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Radiation Detector Hardware: Design Principles and Methodology

Thursday, 18 February 2021 11:00 (2:00)

Content

Summary

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Session Classification: Parallel session - IAEA Project: Data Acquisition System for Radiotracer Techniques in Industrial Applications and Environmental Monitoring