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Citizen Science with Application to Nuclear, Seismic and Air Quality Monitoring: Applications | (smr 3596)

Wednesday 17 March 2021

Nuclear Radiation Track: Create Embedded Processor Design with a Block Diagram - Online
(13:00-15:15)

time	title	presenter
13:00	Introduction to Integrated Development Environment for FPGA	MLADEN BOGOVAC
13:45	Lab Exercise	MLADEN BOGOVAC (IAEA, AUSTRIA), NIKOLA JOVALEKIC (TTTECH COMPUTERTECHNIK AG, AUSTRIA)