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# Joint ICTP, SAIER and UNESP School on Systems-on-Chip, Embedded Microcontrollers and their Applications in Research and Industry | (smr 3557)

## Wednesday 20 October 2021

### **Session 3** - Online (14:00-18:30)

***Chair: Andre CASCADAN***

| time  | title  | presenter  |
|-------|--|--|
| 14:00 | SoC Architecture and Design Methodology              | SISTERNA, Cristian   |
| 15:00 | Fundamentals of VHDL (Hardware Description Language) | MELO, Rodrigo  |
| 16:00 | VHDL for Synthesis                                   | MELO, Rodrigo  |
| 17:00 | MCU Lab exercises and Tutors                         | LUIGI CALLIGARIS AND<br>ANDRE CASADAN<br>(NCC-UNESP, BRAZIL) |