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Joint ICTP, SAIER and UNESP School on Systems-on-Chip, Embedded Microcontrollers and their Applications in Research and Industry | (smr 3557)

Monday 25 October 2021

Session 6 - Online (14:00-19:00)

Chair: *Maria Liz CRESPO*

time	title	presenter
14:00	High Level Synthesis (HLS)	RINCON CALLE, Fernando
15:30	Setup of the remote SoC-FPGA Lab	GARCIA ORDONEZ, Luis
16:30	Lab Tutorial	MORALES, Ivan
17:30	SoC-FPGA Lab exercises with Tutors	LUIS GARCIA, WERNER FLORIAN, BRUNO VALINOTI, ROMINA MOLINA (UNIVERSITY OF TRIESTE/ICTP), IVAN MORALES (ICTP), CRISTIAN SISTERNA (UNIVERSITY OF SAN JUAN, ARGENTINA)