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Joint ICTP, SAIFR and UNESP School on Systems-on-Chip, Embedded Microcontrollers and their Applications in Research and Industry | (smr 3557) / Programme

Tuesday 26 October 2021

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Session 7 - Online (14:00-19:00)

Chair: Luigi CALLIGARIS

time	title	presenter
14:00	New SoC platforms for AI and Algorithm Accelerationl	SUTTER, Gustavo
15:30	Reconfigurable Virtual Instrumentation (RVI) based on SoC-FPGA	CICUTTIN, Andres
16:30	The RVI communication block: ComBlock	MELO, Rodrigo
17:30	SoC-FPGA Lab exercises with Tutors	LUIS GARCIA, WERNER FLORIAN, BRUNO VALINOTI, ROMINA MOLINA (UNIVERSITY OF TRIESTE/ICTP), IVAN MORALES (ICTP), CRISTIAN SISTERNA (UNIVERSITY OF SAN JUAN, ARGENTINA)

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