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Joint ICTP, SAIER and UNESP School on Systems-on-Chip, Embedded Microcontrollers and their Applications in Research and Industry | (smr 3557)

Wednesday 27 October 2021

Session 8 - Online (14:00-19:00)

Chair: Andre CASCADAN

time	title	presenter
14:00	FPGA for the Acceleration of Machine Learning Algorithms	MOLINA, Romina
15:30	Integrating a real-time operating system and a Linux one in a ZynqMP system	SANDER, Oliver
16:30	SoC-FPGA Development Framework: UDMA & CLI	FLORIAN, Werner
17:30	SoC-FPGA Lab exercises with Tutors	LUIS GARCIA, WERNER FLORIAN, BRUNO VALINOTI, ROMINA MOLINA (UNIVERSITY OF TRIESTE/ICTP), IVAN MORALES (ICTP), CRISTIAN SISTERNA (UNIVERSITY OF SAN JUAN, ARGENTINA)