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Joint ICTP, SAIER and UNESP School on Systems-on-Chip, Embedded Microcontrollers and their Applications in Research and Industry | (smr 3557)

Friday 29 October 2021

Session 10 - Online (14:00-19:00)

Chair: *Maria Liz CRESPO*

time	title	presenter
14:00	FOSS (Free Open Source Software) for FPGA	MELO, Rodrigo
15:00	SoC-FPGA DAQ System: Demo	GARCIA ORDONEZ, Luis
16:00	SoC-FPGA Lab exercises with Tutors	LUIS GARCIA, WERNER FLORIAN, ROMINA MOLINA (UNIVERSITY OF TRIESTE/ICTP), IVAN MORALES (ICTP), CRISTIAN SISTERNA (UNIVERSITY OF SAN JUAN, ARGENTINA)
18:00	ICTP Programmes	CRESPO, Maria Liz
18:30	Closing Remarks by School Directors	