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# Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765)

## Monday 14 November 2022

- Kastler Lecture Hall (AGH) (09:30-12:30)

| time  | title                                    | presenter   |
|-------|--|---|
| 09:30 | Welcome to the School                    | MARIA LIZ CRESPO (ICTP, ITALY), KALLIOPI KANAKI (IAEA, AUSTRIA), ANDRES CICUTTIN, (ICTP, ITALY), MLADEN BOGOVAC (IAEA, AUSTRIA) |
| 09:40 | School Outline                           | MARIA LIZ CRESPO  |
| 09:50 | Brief introduction from participants     |   |
| 10:00 | Coffee break                             |   |
| 10:30 | Logic design and finite state machines   | PIROUZ BAZARGAN SABET   |
| 11:30 | FPGA and System-on-Chip (SoC) technology | CRISTIAN SISTERNA   |