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# Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765)

## Monday 14 November 2022

- Kastler Lecture Hall (AGH) (14:00-17:30)

time	title	presenter
14:00	Digital numbers representation: fixed point vs floating point	PIROUZ BAZARGAN SABET
15:00	SoC architecture and design methodology	CRISTIAN SISTERNA
16:00	Coffee break	
16:30	SoC architecture and design methodology (cont.)	CRISTIAN SISTERNA