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# Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765)

## Wednesday 16 November 2022

- Kastler Lecture Hall (AGH) (14:00-18:00)

time	title	presenter
14:00	Fundamentals of VHDL (Hardware Description Language)	CRISTIAN SISTERNA
15:00	VHDL for Synthesis	CRISTIAN SISTERNA
16:00	Coffee break	
16:30	Introduction to Lab Activities	LUIS GARCIA ORDONEZ
17:00	Lab 1: Hello World	