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Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765)

Thursday 17 November 2022

- Kastler Lecture Hall (AGH) (14:00-18:00)

| time | title | presenter |
|-------|------------------------------|------------------------|
| 14:00 | High Level Synthesis (HLS) | FERNANDO RINCON CALLE |
| 16:00 | Coffee break | |
| 16:30 | Guide for Lab 2: GPIO In/Out | MAYNOR BALLINA ESCOBAR |
| 17:00 | Lab 2: GPIO In/Out | |