Table of contents

i

Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765)

Friday 18 November 2022

- Kastler Lecture Hall (AGH) (14:00-18:00)

time	title	presenter
14:00	VHDL for Simulation (Testbench)	CRISTIAN SISTERNA
15:00	Lab 3: Custom IP and VHDL	
16:00	Coffee break	
16:30	Lab activities with tutors	

Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765) / Programme
Friday 18 November 2022

Page 1