Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765)

Contribution ID: 145 Type: not specified

The FloPoCo arithmetic core generator

Thursday, 24 November 2022 09:00 (1:00)

Content

Summary

Presenter(s): FLORENT DUPONT DE DINECHIN (National Institut of Applied Sciences, France)

Session Classification: not yet classified