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Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765)

Thursday 24 November 2022

- Kastler Lecture Hall (AGH) (09:00-12:30)

time	title	presenter
09:00	The FloPoCo arithmetic core generator	FLORENT DUPONT DE DINECHIN
10:00	Coffee break	
10:30	The FloPoCo arithmetic core generator (cont.)	FLORENT DUPONT DE DINECHIN
11:30	FreeRTOS Tutorial	HEINRICH RONGEN