Table of contents

i

Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765)

Friday 25 November 2022

- Kastler Lecture Hall (AGH) (14:00-18:00)

time	title	presenter
14:00	Guide for Lab 8: SoC-FPGA DAQ System and TCL	IVAN MORALES ARGUETA
14:30	Lab 8: ADC-DAC loopback and TCL	
16:00	Coffee break	
16:30	Lab activities with tutors	

Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765) / Programme   Friday 25 November 2022

Page 1