# Project Activities

- **Data Acquisition and Processing Systems for Radiation Detectors**

- **Advanced Digital Pulse Processing Methods on FPGA**

- **Ionization Radiation Measurement**

Project 1

Group 1:

SULISTYO Jos	Indonesia
HIJEJ Salim	Syria (Lebanon)
AL-MOHAMMAD Hussein	Jordan
BARIE Exavier	Tanzania
MATTENET Mariana	Argentina
CATALANO Nicolas	Argentina
TATES MONTENEGRO Galo	Ecuador
HAZOU Eyakifama	Togo
BAJWA Fatima	Pakistan
CALVILLO Rogelio	Mexico
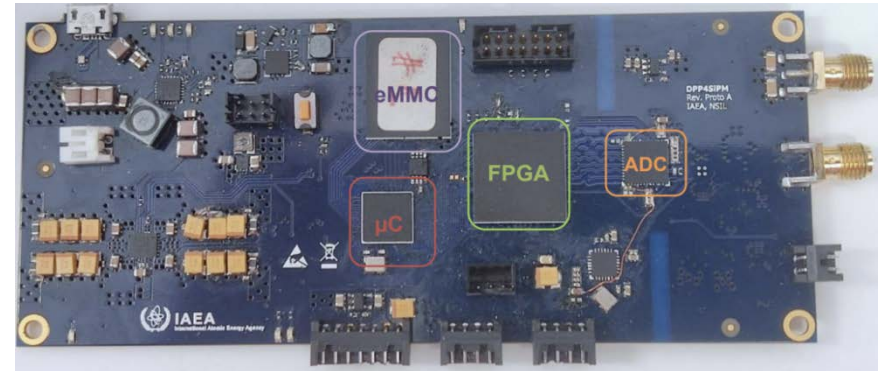
Custom-designed FPGA-uC board

M. Bogovac, N. Jovalekic, I. Morales

Project 2

Group 2:

SRIVASTAVA Geetika	India
RESTREPO Valentina	Colombia
BOETTE Alan	Argentina
DEMIRAJ Gani	Albania
BARRIOS DE LEÓN Gabriel	Guatemala
MALWATHTHA Sameera	Sri Lanka
AHMAD Shamim	Bangladesh
CHOWDHURY Md Nakib Hayat	Malaysia
LOPEZ HERRERA Saul	Guatemala
MONTOYA Sebastián	Colombia
PASTRANA Mario	Brazil
KHATUN Hiramoni	India
SHARMA Chayanika	India
ORTIZ ROSALES Alberto	Mexico
KALANTAI Olha	Ukraine
ZAIACHKOVSKA Iryna	Ukraine
BOURENANE Abdelghani	Algeria
NOVOR Rene	Ghana

TCHAHOU TCHENDJEU Achille	Cameroon
OLUWATOPE Ayodeji	Nigeria
MEMON Md Saad	India
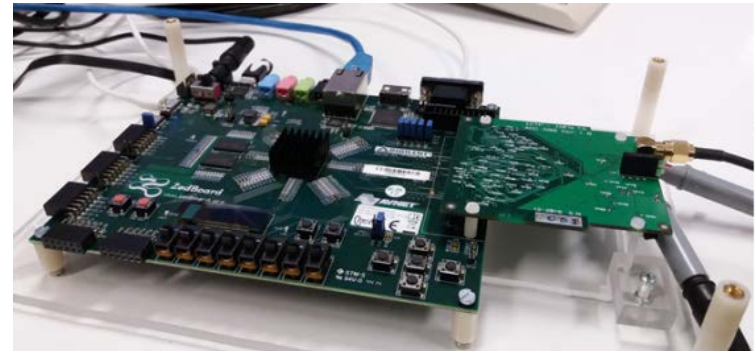
SoC-FPGA Zedboard

Custom-designed high-speed ADC500 board

L. Garcia, W. Florian, M. Ballina, B. Valinoti

Project Presentations

The three best presenters will receive an award of Euro 200 each.

- **PPT presentations of about 5 min on the School's exercises and project activities**

- **The goal of the presentation is to communicate your personal experience including:**

  - **Practical activities that you preferred and considered more relevant for your research**

  - **New topics learned during exercises and project activities sessions**

  - **Brief description of the project and implemented solution**

  - **Difficulties and challenges afforded**

  - **Your personal improvement to the implemented solution**

  - **Other personal comments and suggestions**

Please confirm your participation by e-mail to mcrespo@ictp.it