

Joint ICTP-IAEA School on FPGA-based SoC and its Applications to
Nuclear and Scientific Instrumentation | (smr 3765)

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Type : **not specified**

Project 1 - Software configuration (bring-up) of the custom-designed hardware platform for DPP and Guide for Lab 1.1

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Content

Summary

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Session Classification : Data Acquisition and Processing Systems for Radiation Detectors