Contribution ID : **175**   Type : **not specified**

Project 1 - FPGA Peripheral Configuration and Microblaze Soft Core Design on Custom-Designed Hardware Platform for DPP and Guide for Lab 1.3

Thursday, 1 December 2022 09:00 (1:00)

Content

Summary

Presenter(s) :   MLADEN BOGOVAC (IAEA, Austria)

Session Classification :   Data Acquisition and Processing Systems for Radiation Detectors