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Joint ICTP-IAEA School on FPGA-based SoC and its Applications to Nuclear and Scientific Instrumentation | (smr 3765)

Thursday 01 December 2022

Data Acquisition and Processing Systems for Radiation Detectors - Kastler Lecture Hall (AGH) (09:00-12:30)

time	title	presenter
09:00	Project 1 - FPGA Peripheral Configuration and Microblaze Soft Core Design on Custom-Designed Hardware Platform for DPP and Guide for Lab 1.3	MLADEN BOGOVAC
09:00	Project-2: SoC-FPGA Data Acquisition and Digital Pulse Processing for Radiation Detectors and Guide for Lab 2.3	LUIS GARCIA ORDONEZ (ICTP, ITALY), BRUNO VALINOTI (ICTP, ITALY), WERNER FLORIAN (ICTP, ITALY)
10:00	Group Photo	
10:05	Coffee break	
10:30	Project activities with tutors	