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Manila, The Philippines
ICTP MICROPROCESSOR LABORATORY ASIAN COURSE
ON ADVANCED VLSI DESIGN TECHNIQUES USING A
HARDWARE DESCRIPTION LANGUAGE | (smr 1441)

Wednesday 27 November 2002

VHDL I - 0-- (11:30-12:30)

time	title	presenter
11:30	VHDL I	N. ABDALLAH