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FIFTH COLLEGE ON MICROPROCESSORS: TECHNOLOGY AND APPLICATIONS
IN PHYSICS

2 - 27 October 1989

Assembly Language Programming

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These notes are intended for internal distribution only.

X, Y - STACKS

```
STACK    ORG $1000
        RMB $100
STACKBS EQU *
```

:

```
LDX #STACKBS
```

:

```
PUSHAX EQU *
```

```
LEAX -1,X
```

```
STA 0,X
```

:

```
PULLAX EQU *
```

```
LDA 0,X
```

```
LEAX 1,X
```

:

:

:

STACK DEFINITION

INITIALIZATION

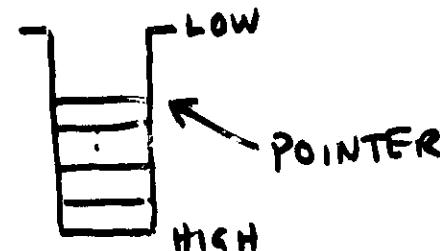
PUT ON X-STACK

TAKE FROM STACK

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STACKS

- DATA STRUCTURE IN MEMORY
- LAST COME FIRST SERVE
 - ACCESS RESTRICTED TO TOP
- POINTER TO TOP
- S, U, X, Y, MEMORY AS POINTERS



- USED FOR SUBROUTINES
TEMPORARIES
SCOPE VARIABLES ETC.

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MOTIVATION FOR SUBROUTINES

- USE WHEN HAVE CODE USED REPEATEDLY

- ## - EXAMPLE

$$S_0 \cdot VINT = \sum_{i=K}^{K+15} i$$

→

LDA K1
JSR SUMINT

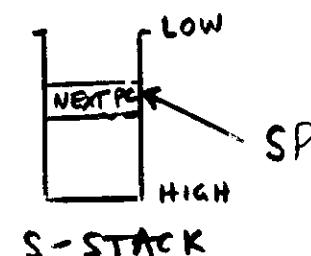
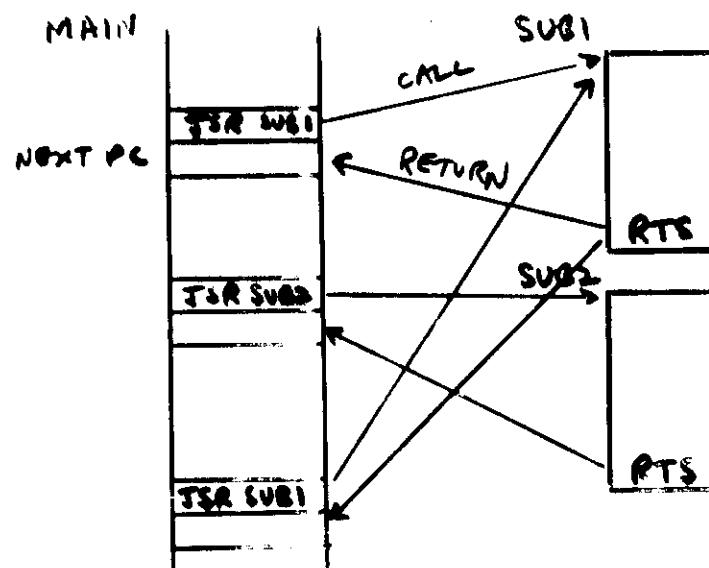
LDA K2
JSR SUMINT

- ADVANTAGE
 - SAVE PROGRAM SPACE
 - LESS CHANCE OF ERROR (MODULAR)
 - CAN SHARE SUBROUTINES

S - STACK (SUBROUTINES) 4

$$JSR \quad EA \quad \equiv \quad \begin{array}{l} SP \leftarrow SP - 1 \\ (SP) \leftarrow PC \\ PC \leftarrow EA \end{array}$$

RTS : $PC \leftarrow (SP)$
 $SP \leftarrow SP + 1$



RAM

RAM
→ CPU

RAM

RAM

RAM (RAM)

RAM

RAM

RAM

RAM

RAM

RAM

RAM

RAM

RAM

⇒ 24,000 inst.

RAM RAM A few instructions in memory

RAM RAM

RAM RAM A few instructions in memory

RAM

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Type of subroutine parameters:

--> VALUE parameter

"value" of the parameter is passed

Example:

numbers, characters, value of expression

restricted to be an "input only" parameter for the subroutine

--> VARIABLE parameter

"address" of the parameter is passed

Example:

address of a variable, start address of an array

"input" and / or "output" parameter for the subroutine

Call a subroutine to initialize an array of a given size to zero:

```
CALL Init ( ArrayAddress, SizeValue )
```

Type of subroutine parameters:

--> Subroutine or Function parameter

the address of a subroutine or a function is passed to the subroutine

Example:

Assume we want to write a subroutine integrating a function over interval [A, B]

The subroutine therefore must obtain function values $f(x)$ for values of x in the interval [A, B]

```
CALL Integrate ( FunctionAddress,  
                  ValueA, ValueB,  
                  ResultAddress      )
```

* only the address of the function is passed

* the parameter definition for the function is unknown !

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Ways to pass parameters to subroutines:

--> processor registers

- * fast and simple
- * limited by the number of registers
- (rewrite fourth programming problem using registers X and Y for input of the interval boundaries and register X to return the result)

--> dedicated memory locations (mail box)

- * associated with the subroutine
- * calling program places input in and retrieves output from the mailbox
- * address has to be known by caller
- * mailbox must be in RAM
(coordination problems)

This method was used in our fourth programming problem !

Ways to pass parameters to subroutines:

--> parameter areas

- * associated with the calling program
- * base address is passed to subroutine using a register
- * possible to use it for different subroutines, hence its size must be big enough to hold the largest number of parameters
- * parameter area must reside in RAM
(coordination problems)
- * "in-line" parameter area is a special case (i.e. return address is address of first parameter)

Example:

BSR	Init	initialize an array
FDB	Bin	address of array Bin
FCB	5	size of array Bin
---		next instruction

Note: Should only be used, if the parameters are "constant"

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Ways to pass parameters to subroutines:

--> stack-oriented parameter passing

- * parameters are "pushed" on the system stack prior to calling the subroutine
- * dynamic allocation (stack overflow !)
- * no worry about RAM coordination

PROBLEMS:

- * order of pushing must be defined
- * space for output parameters
- * addressing of parameters
- * who "cleans" the stack ?

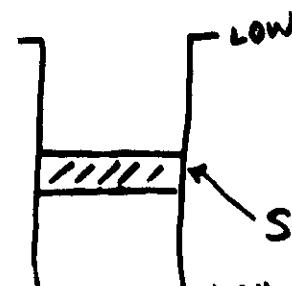
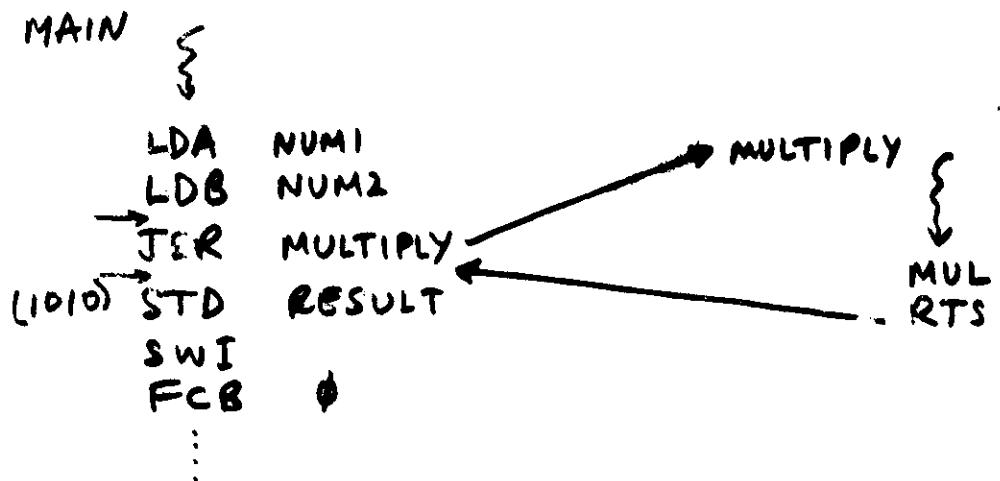
Example:

```
LDA    #5      push size of Bin
STA    -8      push address of Bin
LDX    #BIN
STX    --8
BSR    Init    get it initialized
LEAS   3,8    remove parameters
```

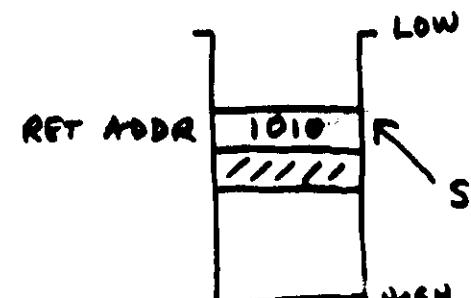
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SUBROUTINES SUMMARY

PARAMETER PASSING IN REGISTERS



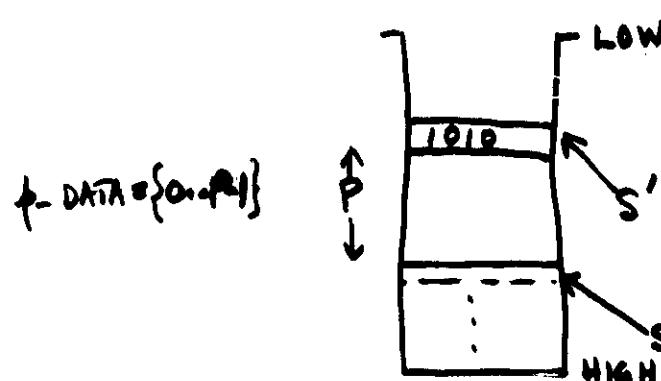
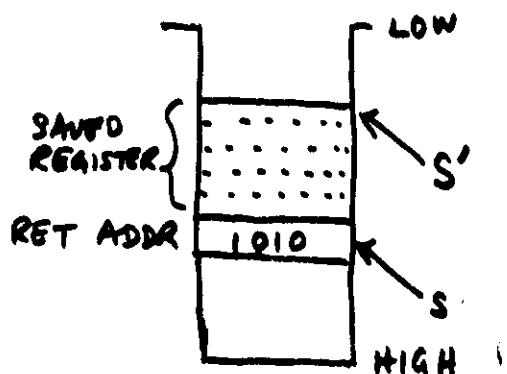
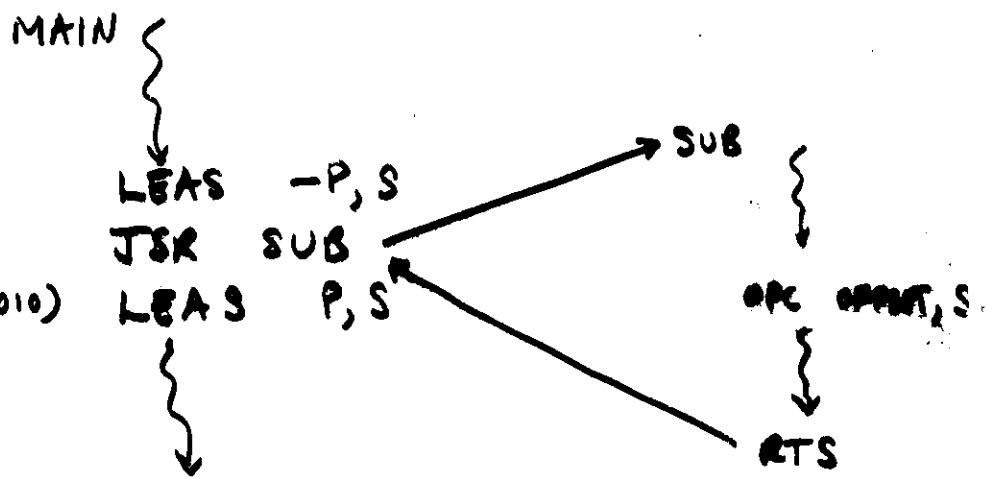
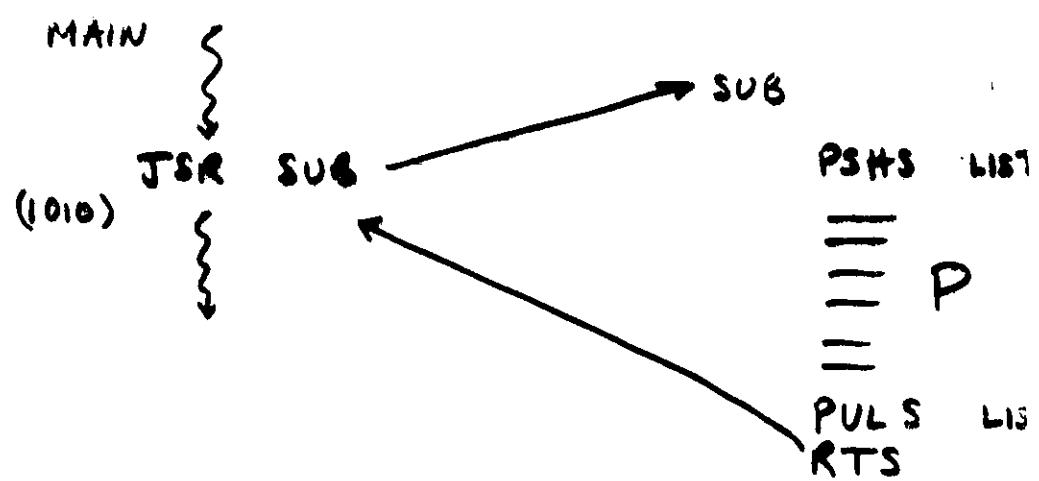
BEFORE JSR
AFTER RTS



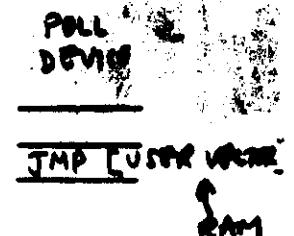
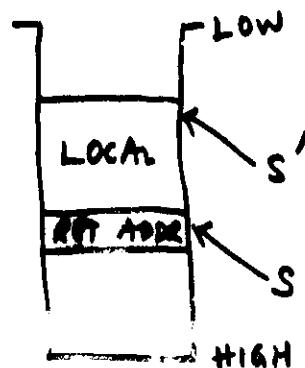
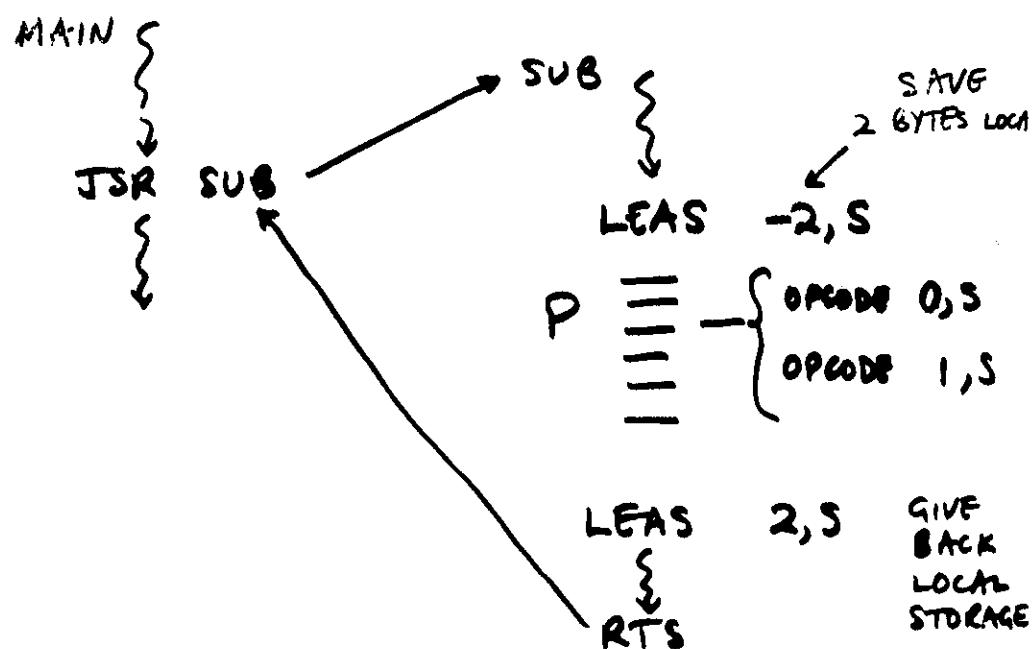
INSIDE
SUBROUTINE

PARAMETERS ON S-STACK

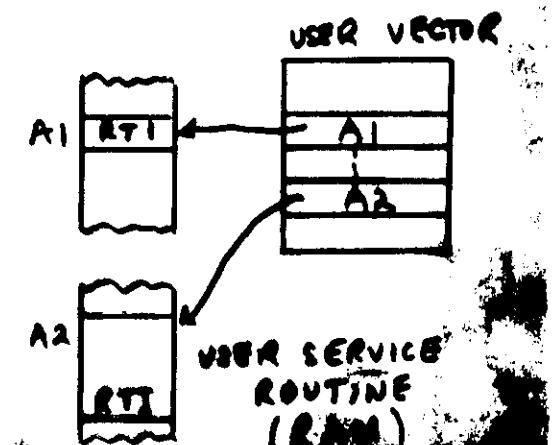
SAVING REGISTERS



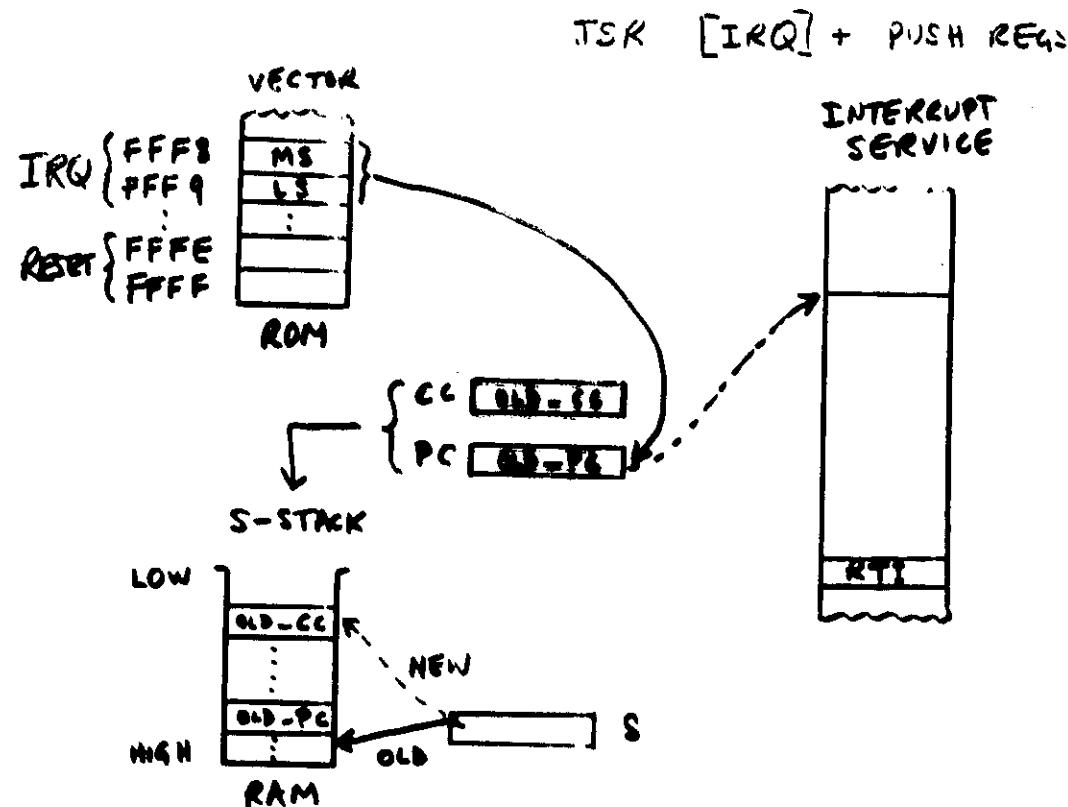
LOCAL STORAGE IN SUBROUTINE



ROSY (ROM)



HW SUBROUTINE CALL - INTERRUPT



RECURSION

PROCEDURE FACT(N)

```
IF N=1 THEN FACT:=1
ELSE FACT:=N*FACT(N-1)
```

* 8-bit NUMBERS

MAIN

```
a → LDB #3
      STB 0,-S
      JSR FACT
      LDA 0,S+
      MUL
      STB RESULT
      SWI
      FCB ♂
```

FACT(N)

* FACT

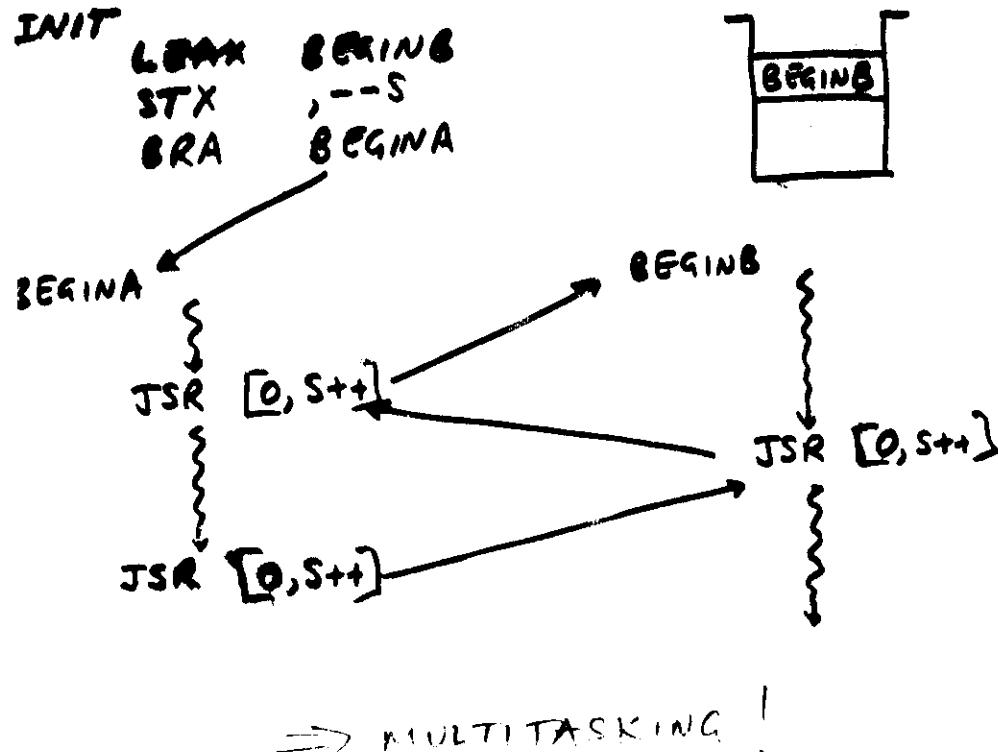
```
b → LDB 2,S
      CMPB #1
      BEQ TERMF
      DECB
      STB 0,-S
      JSR FACT
      LDA 0,S+
      MUL
      RTS
      LDB #1
```

IF N > 1
THEN
FACT(N-1)

ELSE
FACT = 1

ROUTINES

- SIMILAR TO SUBROUTINES
- BOTH ROUTINES ARE SUBROUTINES TO EACH.
- A SCHEDULING MECHANISM.



POSITION INDEPENDENT PROGRAMMING

- RATIONAL
 - COPY PROGRAM TO ANY ADDRESS
- ADVANTAGE
 - PORTABLE
 - USEFUL FOR ROM USAGE
- CHARACTERISTICS
 - SEPARATION OF PROGRAM/DATA
 - ⇒ 1. NO ABSOLUTE ADDRESSES
 - 2. VARIABLES ON U-STACK
 - 3. CONSTANTS IN PROGRAM.

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POSITION INDEPENDENT Program 1 (MODULO)

	ORG	ϕ	
C	RAB	1	PROGRAM
D	RAB	1	VARIABLES
E	RAB	1	
SIZE	EQU	*	
	ORG	\$200	
START	EQU	*	
	LEAU	-SIZE,u	GET SPACE ON U-STACK
	LDA	C,u	
	ADD	DD,u	
	STA	E,u	
WHL	EQU	*	
	CMPA	N,PCR	
	BLO	EXIT	
	SUBA	N,PCR	
	BRA	WHL	
EXIT	EQU	*	
	LEAU	SIZE,u	RETURN SPACE ON U-STACK
	SWI		
	FCB	ϕ	
N	FCB	\$8	
	END		

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U-STACK (TEMPORARY VARIABLES)

ORG	ϕ	
CWD	RAB	2
XYZ	RAB	1
UVW	RAB	4
	:	
SIZE	EQU	*
BUF	EQU	\$E264
FP	EQU	\$0C
ORG	\$200	
START	EQU	*
	LEAU	-SIZE,u
	:	
LDX	CWD,u	
STA	XYZ,u	
	:	
EXIT	LEAU	SIZE,u
	SWI	
	FCB	ϕ
	:	

} PROGRAM
} ON U-STACK
} CONSTANTS

ABSOLUTE ADDRESS Program

(MODULO ARITH)

```

ORG $200
START LDA C      E := C + D
       ADDA D
       STA E

WHL EQU *
       CMPA N
       BLO EXIT
       SUBA N      E := E - N
       BRA WHL    END WHILE
       STA E      SAVE E
       SWI
       FCB $0

CDD RMB 1
       RMB 1
       RMB 1
N     FCB $8
       END
  
```

POSITION INDEPENDENT CODE

(RELATIVE TRANSFER VECTORS)
[CASE]

```

MAIN ORG $200
      EQU *
      ;
      LEAX TABLE, PCR
      LDD B, X
      LEAX MAIN, PCR
      JMP D, X

TABLE EQU *
      FDB SUB1-MAIN
      FDB SUB2-MAIN
      ;
      SUB1 EQU *
      ;
      SUB2 EQU *
      ;

ROUTINES
  
```

CALLING SEQUENCE

RELATIVE VECTOR

END

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Macro - definition and use:

- * macro assigns a name to an instruction sequence

- * use of the name causes assembler to insert the instruction sequence into your program

--> TwoComp, a macro to compute two's complement of accumulator D

* macro definition:

```
TwoComp    MACRO
```

```
    COMB          complement low byte  
    COMA          complement high byte  
    ADDD #1      now two's complement  
ENDM
```

* use of macro:

```
    LDD    Value1  to be complemented  
    TwoComp  
    STD    Value2  two's complement
```

Macro and parameters:

- * macros allow parameter substitution

- * parameters are indicated in the body by:

- $\$1, \$2, \dots, \$9$

- thus allowing up to 9 parameters

- * these 'formal' parameters are replaced by the 'actual' parameters used in the macro call

```
MACNAME <par.1>, <par.2>, ... , <par.9>
```

NOTE:

- * macros must be defined before they are used

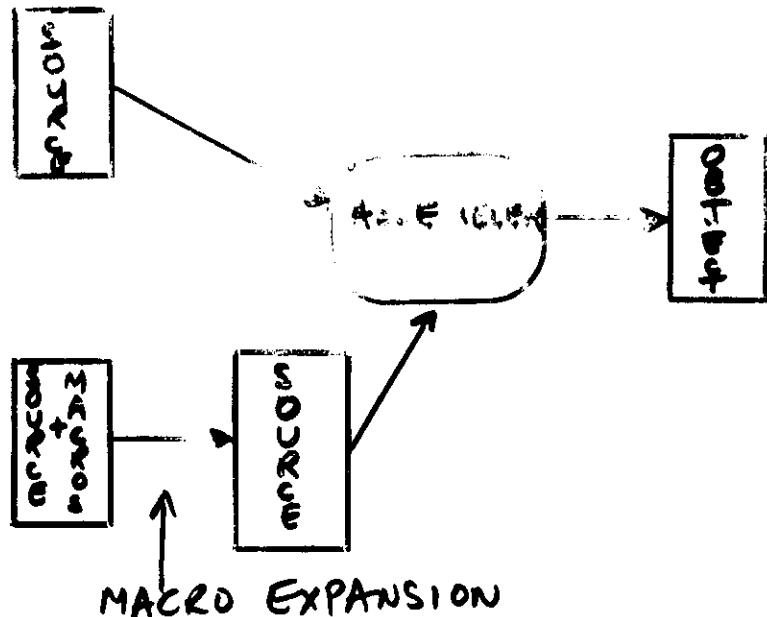
- * local labels are not supported !

- * number of arguments used in a macro call is not available

SEE:

chapter VII of TSC 6809 Assembler manual for more details ...

MACROS



- REPEATED SEQUENCE OF STATEMENTS (VARIANTS)
- TEXTUAL SUBSTITUTION
- EXISTS ONLY BEFORE ASSEMBLY
- NOT A SUBROUTINE
(MACROS HAVE NO LIFE AT EXECUTION TIME)

Define macro with parameters:

--> TwoComp compute 2's complement of first argument and store result into second argument

* macro definition

TwoComp MACRO

LDD	#1	number to complement
COMB		complement low byte
COMA		complement high byte
ADDD	#1	now two's complement
STD	#2	store 2's complement

ENDM

* use of macro now:

TwoComp Value1, Value2

NOTE:

- * we expect to obtain two memory addresses !
- * there is an error in the macro !

MACKEL EXAMPLE

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```
TEST MACRO
LDA #$ &1
LDB L &1
NOP
NOP
NOP
&3
```

```
&4 TST M&1 M
ENDM
```

COMMENTS &2

TEST 1000, 'LIKE THIS', SEX, "LABEL"

```
LDA #$1000
LDB L 1000
NOP
NOP
NOP
SEX
```

COMMENTS LIKE THIS

LABEL TST M1000M

Macro definition - Conditional assembly:

MON - macro for Rosy monitor requests

* define range of monitor requests

MonMin	EQU	0	lowest request
MonMax	EQU	46	highest request
MonStop	EQU	1	stop execution
	SPC	3	

* define macro to handle monitor requests

MON	MACRO		
MonPar	SET	&1	get request number

* assert parameter is valid

MonPar	IF	MonPar < MonMin	
	ERR	too small for Rosy	
	SET	MonStop	
	ELSE		
	IF	MonPar > MonMax	
	ERR	too large for Rosy	
	SET	MonStop	
	ENDIF		
	ENDIF		

* generate the monitor call

SWI			
FCB			
ENDM	MonPar		

MONI MACRO

SWI
FCB & I
ENDM

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Macros - their advantage and disadvantage

Advantages of macros:

- * shorter source programs
- * once debugged --> code error free whenever used
- * easier to implement changes
- * user interface same, but macro body may change (useful in collaborations)
- * extend or clarify instruction set

Disadvantages of macros:

- * macro is expanded every time it is used
may waste memory
- * single macro may generate lot of instructions
- * macro call might have hidden effects ...
(registers, flags)

START

MON 17 INCHW

MON 20 OUTCH

MON 26 PRINT

END

LIB MONCALLS

START

MON INCHW

MON PRINT

END

186 r. ALEKO (X1000)