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# Microprocessor Laboratory Second Central American Regional Course on Advanced VLSI Design Techniques (Puebla, Mexico) | (smr 1590)

## Thursday 16 December 2004

### Basic Building Blocks for Analog Design (1 & 2) - 0-- (09:00-11:00)

time	title	presenter
09:00	Basic Building Blocks for Analog Design (1 & 2)	G. ANELLI