



# Table of contents

Tuesday 14 December 2004 .....	1
--------------------------------	---

# Microprocessor Laboratory Second Central American Regional Course on Advanced VLSI Design Techniques (Puebla, Mexico) | (smr 1590)

## Tuesday 14 December 2004

### Noise and Matching in CMOS (Analog) Circuits (1&2) - 0-- (09:00-11:00)

time	title	presenter
09:00	Noise and Matching in CMOS (Analog) Circuits (1&2)	G. ANELLI