

Table of contents

Wednesday 27 July 2005	1
------------------------------	---

ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Wednesday 27 July 2005

Actel ProASIC Plus FPGA Architecture (09:00-10:30)

time	title	presenter
09:00	Actel ProASIC Plus FPGA Architecture	NIZAR ABDALLAH