

Table of contents

Wednesday 27 July 2005	1
------------------------------	---

ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Wednesday 27 July 2005

Libero IDE Overview and Design Flow (14:30-16:00)

time	title	presenter
14:30	Libero IDE Overview and Design Flow	NIZAR ABDALLAH