

ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional
Microelectronics Workshop on FPGA and VHDL for Research and Training
in Africa | (smr 1691)

Contribution ID : 11

Type : **not specified**

Design Entry

Wednesday, 27 July 2005 16:00 (1:30)

Content

Summary

Primary author(s) : NIZAR ABDALLAH

Presenter(s) : NIZAR ABDALLAH

Session Classification : Design Entry