

Table of contents

Wednesday 27 July 2005	1
------------------------------	---

ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Wednesday 27 July 2005

Design Entry (16:00-17:30)

time	title	presenter
16:00	Design Entry	NIZAR ABDALLAH