## ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Contribution ID: 13 Type: not specified

## **Design Verification and Timing Concepts**

Thursday, 28 July 2005 09:00 (1:30)

Content

Summary

 $Presenter(s): \quad {\rm NIZAR\ ABDALLAH}$ 

Session Classification: Design Verification and Timing Concepts