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ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Thursday 28 July 2005

Design Verification and Timing Concepts (09:00-10:30)

time	title	presenter
09:00	Design Verification and Timing Concepts	NIZAR ABDALLAH