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ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Friday 29 July 2005

Selected Topics on FPGA Design and Logic Synthesis I (16:30-18:00)

time	title	presenter
16:30	Selected Topics on FPGA Design and Logic Synthesis I	ANDRES CICUTTIN