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ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Monday 01 August 2005

Selected Topics on FPGA Design and Logic Synthesis II (14:30-16:00)

time	title	presenter
14:30	Selected Topics on FPGA Design and Logic Synthesis II	ANDRES CICUTTIN