

Table of contents

Monday 25 July 2005	1
---------------------------	---

ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Monday 25 July 2005

Synthesis II - Introduction to VHDL (16:30-18:00)

time	title	presenter
16:30	Synthesis II - Introduction to VHDL	NIZAR ABDALLAH