

Table of contents

Thursday 04 August 2005	1
-------------------------------	---

ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Thursday 04 August 2005

Laboratory (09:00-10:30)

time	title	presenter
09:00	Laboratory	