

Table of contents

Friday 05 August 2005	1
-----------------------------	---

ICTP-INFN Microprocessor Laboratory ICTP-KNUST Regional Microelectronics Workshop on FPGA and VHDL for Research and Training in Africa | (smr 1691)

Friday 05 August 2005

Laboratory (14:30-16:00)

time	title	presenter
14:30	Laboratory	