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# ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

**Tuesday 28 November 2006**

**Synthesis III - Advanced VHDL - Adriatico Guest House Giambiagi Lecture Hall (11:00-12:00)**

time	title	presenter
11:00	Synthesis III - Advanced VHDL	NIZAR ABDALLAH