

**ICTP-INFN Advanced Training Course on FPGA Design and VHDL for
Hardware Simulation and Synthesis | (smr 1780)**

Contribution ID : **60**

Type : **not specified**

Digital Signal Processing and Radar Applications III

Wednesday, 13 December 2006 09:30 (1:00)

Content

Summary

Primary author(s) : KONSTANTIN LUKIN

Presenter(s) : KONSTANTIN LUKIN

Session Classification : Digital Signal Processing and Radar Applications III