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# ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

## Monday 18 December 2006

**Laboratory Session: Projects and Digital Signal Processing with FPGA - Adriatico Guest House Informatics Lab. (09:30-18:00)**

time	title	presenter
09:30	Laboratory Session: Projects and Digital Signal Processing with FPGA	MARIA LIZ CRESPO,ALEXANDER SHAPRIO,ANDRES CICUTTIN