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ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Wednesday 29 November 2006

**Synthesis III - Advanced VHDL (cont.) - Adriatico Guest House Giambiagi Lecture Hall
(09:30-10:30)**

time	title	presenter
09:30	Synthesis III - Advanced VHDL (cont.)	NIZAR ABDALLAH