



# Table of contents

Wednesday 29 November 2006 .....	1
----------------------------------	---

# ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

## Wednesday 29 November 2006

**Digital Design III (more complex elements: RAM, ROM, buses, pipeline concept, etc) - Adriatico Guest House Giambiagi Lecture Hall (11:00-12:00)**

time	title	presenter
11:00	Digital Design III (more complex elements: RAM, ROM, buses, pipeline concept, etc)	PIROUZ BAZARGAN-SABET