

Table of contents

Thursday 30 November 2006	1
---------------------------------	---

ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Thursday 30 November 2006

**Design verification and timing concepts - Adriatico Guest House Giambiagi Lecture Hall
(09:30-10:30)**

time	title	presenter
09:30	Design verification and timing concepts	NIZAR ABDALLAH