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ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Thursday 30 November 2006

(LiberioTM IDE) Timing Constraints and Analysis - Adriatico Guest House Giambiagi Lecture Hall (11:00-12:00)

time	title	presenter
11:00	(LiberioTM IDE) Timing Constraints and Analysis	NIZAR ABDALLAH