

**ICTP-INFN Advanced Training Course on FPGA Design and VHDL for  
Hardware Simulation and Synthesis | (smr 1780)**

Contribution ID : 21

Type : **not specified**

## **Programmable logic & FPGA architectures**

*Friday, 1 December 2006 09:30 (1:00)*

### **Content**

### **Summary**

**Primary author(s) :** NIZAR ABDALLAH

**Presenter(s) :** NIZAR ABDALLAH

**Session Classification :** Programmable logic & FPGA architectures